Total No. of Questions: 8]	26	SEAT No. :	
P2959		[Total No. of Pa	ges: 2

[5669]-549 T.E. (E & TC) ADVANCED PROCESSORS (2015 Pattern) (Semester - II)

Time: 2½ Hours] [Max. Marks: 70 Instructions to the candidates:

- 1) Neat diagrams must be drawn wherever necessary.
- 2) Figures to right indicate full marks.
- 3) Assume suitable data if necessary.
- 4) All questions are compulsory.
- Q1) a) Justify suitability of ARM in embedded applications. [6]
 - b) Explain pin connect block of ARM. State the significance of PINSEL register. [6]
 - c) Interface 8 LEDs with port 0 of LPC 2148. Write an embedded 'C' program for following operations. (Consider a suitable delay). [8]
 - i) Glow all LEDs
 - ii) Glow alternate LEDs
 - iii) Glow extreme end LEDs (i.e. LED1 & LED 8)

Repeat above operations continuously.

OR

- Q2) a) State & explain the registers of ARM processor. Also explain the significance of SPSR. [6]
 - b) Explain with a neat schematic the system control block of LPC 2148.[6]
 - c) Draw & explain interfacing of GLCD with LPC 2148 with the help of flowchart /algorithm. [8]
- Q3) a) Draw & explain interfacing of I2C based EEPROM with LPC 2148.[8]
 - b) Explain VIC based on chip ADC interfacing with LPC 2148. [8]

P.T.O.

Explain SD card interfacing with LPC 2148 with the help of interfacing **Q4**) a) diagram and flowchart. Explain GSM interfacing with LPC2148. Write an embedded 'C' program b) to send a message. Explain the architecture of TMS 320C67X with the help of neat **Q5**) a) diagram. [10] Explain in detail the functional units of C67X. b) [8] OR Explain the following architectures. [9] **Q6**) a) i) Harvard b) List various registers of C67X. Also explain AMR & CSR register of C67X. [9] Explain in detail - parallel operation & pipeline operation of C67X. [8] **Q7**) a) Explain the function of Following instructions: [8] b) MPYU. M1 or . M2 i) MVKLH. S1 or. S ii) SADD .L1 or . L2 iii) LDBU. D1 or .D2 OR (Q8) a) Write a note on: Conditional operations. Internal memory. ii) Explain in detail-fixed point Instructions & Floating point instructions.[8]