Total No	of Questions :6]	SEAT No.:			
P217	· Section of the sect	L	No. of Pages :2		
1 217	Oct./ BE/ Insem 533	[10441]	to. of fages .2		
B.E. (Electrical)					
PLC AND SCADA APPLICATIONS					
(2015 Course) (Semester - I) (403142)					
(2013 Course) (Bennester - 1) (403142)					
Time: 1	Hour]	[N	Max. Marks :30		
Instruction	ons to the cardidates:				
1)	Answer Q1 or Q2, Q3 or Q4, Q5 or Q6.		So.		
2)	Neat diagrams must be drawn wherever necessary.	8			
3)	Figures to the right indicate full marks.				
01) -)		6 CD			
<b>Q1</b> ) a)	List & discuss the advantages & disadvantage Controller.	es of Program	imable Logic [8]		
			[0]		
b)	Which are various selection criteria for PLC?	)	[2]		
	OR				
<b>Q2</b> ) a)	Explain the types and the function of program	nmer and mor	nitors. [5]		
b)	Explain input and output modules in PLC.		[5]		
	6.				
	9.7				
<b>Q3</b> ) a)	Explain various output ON/OFF devices.		[5]		
b)	Explain linear variable differential transform	mer (LVDT)	with proper		
0)	diagram.		[5]		
		0,01			
	OR	3			
<b>Q4</b> ) a)	What are different types of actuators? Explain	n any one of th	nem in detail.		
		26	[5]		
b)	Write a short note on encoders explaining its	types	[5]		
U)	Write a short note on encoders explaining its	types.	[5]		
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			<i>P.T.O.</i>		

<b>Q</b> 5) a)	Also draw its timing diagram.	g with its bits. [8]
b)	State some applications using timers in PLC.	[2]
<b>Q6</b> ) a)	OR What is Master control relay (MCR)?	[3]
	Develop the ladder diagram for the combination of Time	
b)	for lamp ON/OFF operation. Generate a delay of 50 sec (TON) for 5 sec along with the counter (UP counter) for sec X 10 counts = 50 sec). The lamp should be ON after	c using Timer 10 counts. (5
	sec.	[7]
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