Total No. of Questions : 8]

## **P9093**

[Total No. of Pages : 2

[Max. Marks : 70

**SEAT No. :** 

## [6179]-21 S.E. Electrical **ANALOG & DIGITAL ELECTRONICS** (2019 Pattern) (Semester-III) (203143)

*Time : 2<sup>1</sup>/<sub>2</sub> Hours*]

Instructions to the candidates: Solve Q1 or Q2, Q3 or Q4, Q5 or Q6, Q7 or Q8. 1)

- 2) Neat diagrams must be drawn wherever necessary
- Figures to the right indicate full marks. 3)
- Use of calculator is allowed. *4*)
- Assume suitable data if necessary. 5)

<i>Q1</i> ) a)	Write a short note on sequential memories (Definition, C	haracteristics,
	Examples).	[6]
b)	Explain Programmable Array Logic in detail	[6]
c)	What is semiconductor memory? Enlist advantages of it.	[5]
	OR	

<b>Q2</b> ) a)	Describe in detail Read only memory (ROM).	[6]
b)	Write a short note on FPGA	[6]
c)	What is DRAM? What are its advantages and disadvantages?	.[5]
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- What is DRAM? What are its advantages and disadvantages? c)
- [6] Explain how sine wave is generated by using Op-amp. *Q3*) a)
  - Draw neat diagram of Op Amp as a Schmitt trigger and explain its b) working. [6]
  - Define the characteristics of practical OPAMI [6] c)

## OR

<i>Q4</i> ) a)	With neat pin diagram explain function of each pin of IC 741.	[6]
b)	Explain working of OPAMP as instrumentation amplifier.	[6]
c)	Draw input and output waveforms of Op Amp as a Zero	crossing
	Detector. Explain its working.	[6]

*P.T.O.* 

- Explain functioning of LM 317 as a voltage regulator. *Q*5) a) [6]
  - With neat diagram explain working of IC 555 as a Monostable b) Multivibrator. [6]

[5]

[6]

- Draw and explain frequency response of high pass filter. c) OR
- With neat diagram explain working of IC 555 as a Astable Multivibrator. **Q6**) a) [6]
  - Draw and explain frequency response characteristic of ideal and practical b) Low Pass Filter [6]
  - What is voltage regulator? Write any two applications of voltage c) regulator. [5]
- Compare single phase Half Wave Rectifier and single phase Full Wave **Q7**) a) Rectifier. [6]
  - With the help of circuit diagram and relevant waveforms, explain the b) Operation of a 3-phase bridge rectifier with resistive load. [6]
  - Define following terms c)
    - Form factor i)
    - ii) **Ripple factor**
    - iii) TUF

- Explain working of single phase half wave rectifier with RL load. Draw **08**) a) output waveforms. 61
  - State values of output Performance parameters of single phase full wave b) 51,002,003,6 bridge rectifier. [6]
    - DC output voltage i)
    - DC output current ii)
    - iii) Output DC power.
    - **Rectification Efficiency** iv)
    - Form Factor V)
    - PIV vi)
  - Explain in detail the working of center tapped rectifier connected to the R c) load. [6]

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