Total No. of Questions: 8]	200	SEAT No.:
P-9701		[Total No. of Pages : 2

## [6179]-244A

## S.E. (Computer Engineering) DIGITAL ELECTRONICS AND LOGIC DESIGN

(2019 Pattern) (Semester - III) (210245)

	(2019 Factorii) (Semester - 111) (210245)	
<i>Time : 2<sup>1</sup>/</i>	[Max. 1	Marks: 70
Instructi	ons to the candidates :	
1)	Solve Q1 or Q2, Q3 or Q4, Q5 or Q6, Q7 or Q8.	<b>3</b> ,
2)	Neat diagrams must be drawn wherever necessary.	
3)	Assume suitable data, if necessary.	
<b>Q1</b> ) a)	What are the advantages of M S JK flip flop? Explain the w	vorking of
~ , ,	MS J-K flip flop in detail.	[6]
b)	State different types of shift registers. Give its applications.	[6]
c)	Draw and explain 3-bit asynchronous down counter usin	g JK flip
ŕ	flop. Also draw the necessary timing diagram.	[6]
	OR	
<b>Q2</b> ) a)	Compare synchronous and Asynchronous counter.	[6]
b)	Explain how JK flip is converted into.	.[6]
	i) D FF	
	i) TEN 26.	
	ii) TFF	
c)	Write short note on Ring counter.	[6]
<b>Q3</b> ) a)	Explain how JK flip is converted into.  i) D FF  ii) T FF  Write short note on Ring counter.  Draw and explain the general structure of PLA.	[6]
b)	Implement following Boolean function using PAL	[6]
	$F(A, B, C, D) = \Sigma m(0, 1, 3, 15)$	
c)	Draw the state diagram, state table, and ASM chart for a 2-	bit binary
- /	counter having one enable line E such that $E = 1$ counting en	•
	E = 0 counting disabled.	[5]
	OB .	

<b>Q4</b> )	a)	What is an ASM Chart? Name the elements of an ASM chart an define each of them.	d [6]
	b)	Implement BCD to Ex-3 code converter using PAL. [6]	<b>6</b> ]
	c)	What is the difference between PAL and PLA. [5	<b>5</b> ]
<b>Q</b> 5)	a)	With the help of a neat diagram, explain the working of two - inpuTTL NAND gate.	
	b)	Define the following terms and mention the standard values for TT logic Family.	
		<ul><li>i) Power dissipation</li><li>ii) Noise margin</li><li>iii) Propagation Delay</li></ul>	
	c)		<b>6</b> ]
	C)	OR	ני
<b>Q6</b> )	a)	Compare TTL and CMOS logic family.	51
ره ک		What is logic family? Give the classification of logic family and als	
	0)		<b>6</b> ]
	c)	Explain the wired logic output of TTL with neat diagram.	<b>6</b> ]
<i>Q7</i> )	a)	What is system bus? Draw microprocessor bus structure and explain i brief.	in [6]
	b)	Write a short note on following with respect to microprocessor.	<b>5]</b> \
		i) Address Bus	)
		ii) Data Bus	
		iii) Control Bus	
	c)	Explain the Memory organization of the microprocessor. [5]	5]
		OR OR	
<i>Q8</i> )	a)	What is microprocessor? List different applications of microprocesso	r.
	)		<b>6</b> ]
	b)	Write a short note on ALU IC 74181.	
	c)	With the help of a block diagram explain the fundamental units of microprocessor.	

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