Seat No.

[5352]-562

S.E. (Com. Engg.)(I-Sem.) EXAMINATION, 2018 DIGITAL ELECTRONICS AND LOCIC DESIGN (2015 PATTERN)

Time: Two Hours

Maximum Marks: 50

- N.B. := (i) Attempt Q.1 or Q.2, Q.3 or Q.4, Q.5 or Q.6, Q,7 or Q.8.
 - (ii) Neat diagram must be draw wherever necessary.
 - (iii) Assume suitable data, if necessary.
- 1. (a) Design two bit comparator using gates (consider A1 MSB and A0 LSB) [4]
 - (b) Minimize the following logic function using K-map and realize using logic gates: [4]

 $F(A,B,C,D) = \sum M(1, 5, 7, 13, 15) + d(0, 6, 12, 14).$

- (c) Design 3-bit synchronous counter using T filp-flop. [4]

 Or
- 2. (a) Design a sequence generator for the sequence 1010 using shift register. [6]
 - (b) Simplify the following function using Qunie-McCluskey minimization technique: [6]

 $Y(A, B, C, D) = \sum_{i=1}^{n} (0, 1, 2, 3, 5, 7, 8, 9, 11, 14).$

P.T.O.

3.	(a)	State and explain basic components of ASM chart. Draw As	SM
		chart for MOD 3 UP counter.	[6]
	(<i>b</i>)	Implement 3 bit binary to gray code converter using PLA Or	.[6]
4.	(a)	Write VHDL code for full adder using data flow modeling style	.[4]
	(<i>b</i>)	Explain entity declaration for 4: 1 multiplexer having ena- line.	ble [2]
	(c)	Design BCD to Excess-3 code converter using PLA.	[6]
5 .	(a) 🗸	Explain with neat diagram CMOS inverter.	[4]
	(b)	State the following characteristics of digital IC logic fam	nily
		TTL and CMOS:	[4]
		(i) FAN out(ii) Noise Margin	
	(<i>c</i>)	Explain TTL open collector logic.	[5]
		Or	
6.	(a)	Give the classification of logic family.	[4]
	(<i>b</i>)	Draw three imput standard TTL NAND gate and explain	its
		operation.	[5]
	(c)	Draw three imput standard TTL NAND gate and explain operation. Explain wired logic in CMOS.	[4]
7.	(a)	Give the significance of the following pins of mirocontrol	llor
1.	(<i>a</i>)		
•		8051 : (i) ALE	[7]

[5352]-562

	(ii) INT1			
	(iii) TXD			
	(vi) PSEN			
	(ii) INT1 (iii) TXD (vi) PSEN (v) EA (vi) WR			
	(vi) WR			
	(vii) RXD.			
(<i>b</i>)	Explain addressing modes of 8051 with example (any	3).[6]		
	Or			
(a)	Which pins of 8051 are used for interrupt. Draw and exp	plain		
1	IF register.	[5]		
(b)	Compare microprocessor and microcontroller.	[2]		
(<i>c</i>)	Explain the following instructions with respective to microcontroller			
	8051 and give example of each:	[6]		
	(i) DIV	.0		
	(ii) L JUMP			
	(iii) PUSH.	XXIII		
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	CARIE ARTONIA			
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